

GOVERNMENT COLLEGE OF ENGINEERING, JALGAON
(An Autonomous Institute of Government of Maharashtra)

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Minutes of Third Meeting of BoS (E and T/C Engineering) held on 20/03/2015

Date: 30/04/2015

The third meeting of all the members of Board of Studies -Electronics and Telecommunication Engineering, Jalgaon was held on 20/03/2015 at 08.00 am in the meeting hall of the institute. The following members were present for the meeting.

Sr. No.	Name of the member
1.	Prof. S. O. Dahad - Chairman
2.	Dr. S. V. Bonde - Member
3.	Dr. K. M. Bhurchandi - Member
4.	Dr. A. J. Patil- Member
5.	Prof. Kaustubh Sakhare- Member
6.	Mr. Devdatta Gokhale -Member
7.	Dr. M. H. Nerkar- Member
8.	Prof. G. K. Andurkar- Member
9.	Prof. A. G. Andurkar- Member
10.	Prof. M. S. Sadavarte- Member
11.	Prof. S. M. Patil- Member
12.	Prof. M. R. Dhotre -Member
13.	Prof. Y. D. Kapse- Member
14.	Prof. S. C. Kulkarni - Member Secretary
15.	Prof.D. M. Gaikwad- Member
16.	Dr. S. N. Talbar - Invited Member

At the start of the meeting, Chairman Prof. S.O. Dahad welcomed Honorable members of Board of Studies. Prof.S.C.Kulkarni Member-Secretary; BoS informed to all the members that Mr. Prashant Marathe had expressed his inability to attend the meeting due to some unavoidable circumstances, and therefore leave of absence was granted to him.

With the permission of chairman the proceeding of BoS started.

Item No 1:

To read and confirm the minutes of second meeting of BoS, Electronics and Telecommunication Engineering held on October 8, 2014 in HoD (E&Tc) cabin.

BoS noted and confirmed minutes of second meeting of BoS.

Item No 2:

To note and approve the action taken by department on the agenda points discussed and resolved in the second meeting of BoS.

BoS noted and approved the action taken by department on the agenda points discussed and resolved in the second meeting of BoS.

Item No 3:

To discuss and approve detailed syllabi of courses ET201 Network Analysis and ET205 Network Analysis Lab for S. Y. B. Tech. (E&Tc).

3.1 Before starting discussion about detailed syllabi of courses, BoS opined that it is necessary to have a look at UG curriculum structure of Electronics and Telecommunication engineering at a glance.

3.2 After going through the structure, following typographic mistakes in the names of courses were pointed out and it was decided to correct these mistakes as:

Sr. No.	Name in the current Structure	Modified Name of the course
1	ET204 Digital Electronics and Microprocessor Fundamentals	ET204 Digital Electronics
2	ET258 Microprocessor and its Interfacing Lab	ET258 Microprocessor and Microcontrollers Lab

3.3 BoS observed that ET255 Object Oriented Programming Lab is a course offered in S. Y. B. Tech. (E&Tc) and ET308 Data Structure Lab is a course offered in T. Y. B. Tech. (E&Tc). BoS opined that Data Structure Lab should be offered in S. Y. B. Tech. (E&Tc) and Object Oriented Programming Lab should be offered in T. Y. B. Tech. (E&Tc). BoS also felt that credits of Data structure Lab should be 4 (02 L+ 02 PR) instead of 3. Hence it was decided that credits of course ET251 Signals and Systems should be reduced from 4 to 3 as 3 credits are sufficient for course Signals and System and one credit of Data structure Lab should be increased at S. Y. B. Tech.

3.4 BoS felt that examination scheme for this Data Structure Lab should be 25 ISE and 50 ESE i.e. total 75 marks. Hence examination scheme of course ET256 Signals and Systems Lab is modified from 25 ISE + 25 ESE to 25 marks ICA.

3.5 Object Oriented Programming Lab is offered in T. Y. B. Tech. instead of data structure Lab. (These two courses are swapped).

3.6 Modified Curriculum structure with all these corrections is enclosed as **Annexure - I**. It was decided to forward these modifications in UG curriculum structure of Electronics and Telecommunication engineering to Academic Council for final approval.

3.7 Detailed syllabi of courses ET201 Network Analysis and ET205 Network Analysis Lab for S. Y. B. Tech. (E&Tc) were approved after thorough discussion with slight modifications. Approved syllabi of these courses are enclosed as **Annexure - II**

Item No 4:

To discuss and approve detailed syllabi of courses ET202 Components, Devices and Instruments Technology and ET206 Components, Devices and Instruments Technology Lab for S. Y. B. Tech. (E&Tc).

Detailed syllabi of courses ET202 Components, Devices and Instruments Technology and ET206 Components, Devices and Instruments Technology Lab for S. Y. B. Tech. (E&Tc) were approved after thorough discussion with slight modifications. Approved syllabi of these courses are enclosed as **Annexure -III**.

Item No 5:

To discuss and approve detailed syllabi of courses ET203 Electronics Devices and Circuits and ET207 Electronics Devices and Circuits Lab for S. Y. B. Tech. (E&Tc).

Detailed syllabi of courses ET203 Electronics Devices and Circuits and ET207 Electronics Devices and Circuits Lab for S. Y. B. Tech. (E&Tc) were approved after thorough discussion with slight modifications. Approved syllabi of these courses are enclosed as **Annexure - IV**.

Item No 6:

To discuss and approve detailed syllabi of courses ET204 Digital Electronics and ET208 Digital Electronics Lab for S. Y. B. Tech. (E&Tc).

- 6.1 Typographical error in the name of course ET204 Digital Electronics and Microprocessor Fundamentals has been modified to ET204 Digital Electronics as discussed in point 3.2
- 6.2 Detailed syllabi of courses ET204 Digital Electronics and ET208 Digital Electronics Lab for S. Y. B. Tech. (E&Tc) were approved after thorough discussion with slight modifications. Approved syllabi of these courses are enclosed as **Annexure – V.**

Item No 7:

To discuss and approve detailed syllabi of courses ET251 Signals and Systems and ET256 Signals and Systems Lab for S. Y. B. Tech. (E&Tc).

- 7.1 Credits of this course ET251 Signals and Systems are modified as discussed in point 3.3 and examination scheme of ET251 Signals and Systems Lab is modified as discussed in point 3.4.
- 7.2 Detailed syllabi of courses ET251 Signals and Systems and ET256 Signals and Systems Lab for S. Y. B. Tech. (E&Tc) were approved after thorough discussion with slight modifications. Approved syllabi of these courses are enclosed as **Annexure – VI.**

Item No 8:

To discuss and approve detailed syllabi of courses ET252 Electronic Circuits and Applications and ET257 Electronic Circuits and Applications Lab for S. Y. B. Tech. (E&Tc).

Detailed syllabi of courses ET252 Electronic Circuits and Applications and ET257 Electronic Circuits and Applications Lab for S. Y. B. Tech. (E&Tc) were approved after thorough discussion with slight modifications. Approved syllabi of these courses are enclosed as **Annexure – VII.**

Item No 9:

To discuss and approve detailed syllabi of courses ET253 Microprocessor and Microcontrollers and ET258 Microprocessor and Microcontrollers Lab for S. Y. B. Tech. (E&Tc).

Detailed syllabi of courses ET253 Microprocessor and Microcontrollers and ET258 Microprocessor and Microcontrollers Lab for S. Y. B. Tech. (E&Tc) were approved after thorough discussion with slight modifications. Approved syllabi of these courses are enclosed as **Annexure – VIII.**

Item No 10:

To discuss and approve detailed syllabi of courses ET254 Analog Communication and ET259 Analog Communication Lab for S. Y. B. Tech. (E&Tc).

Detailed syllabi of courses ET254 Analog Communication and ET259 Analog Communication Lab for S. Y. B. Tech. (E&Tc) were approved after thorough discussion with slight modifications. Approved syllabi of these courses are enclosed as **Annexure – IX.**

Item No. 11

To discuss and approve detailed syllabus of course ET255 Data Structure Lab for S. Y. B. Tech. (E&Tc).

11.1 BoS decided to offer Data structure Lab for S. Y. B. tech. (E&Tc) instead of Object Oriented Programming Lab and modify its credits as discussed in point 3.3. Examination scheme of this course is modified as per discussion in point 3.4.

11.2 Detailed syllabus of course ET255 Data Structure Lab for S. Y. B. Tech. (E&Tc) was approved after thorough discussion with slight modifications. Approved syllabus of this course is enclosed as **Annexure - X**.

11.3 On approval of detailed syllabi of all courses, BoS approved equivalency for courses of autonomous and NMU curriculum as proposed by DFB. It is enclosed as **Annexure - XI**

Item No 12:

To discuss and approve detailed syllabi of courses ET221 Digital Electronics & Logic Design and ET222 Digital Electronics & Logic Design Lab for S. Y. B. Tech. (Computer).

Detailed syllabi of courses ET221 Digital Electronics & Logic Design and ET222 Digital Electronics & Logic Design Lab for S. Y. B. Tech. (Computer) were approved after thorough discussion with slight modifications. Approved syllabi of these courses are enclosed as **Annexure - XII**.

Item No 13:

Any other point by the permission of chair.

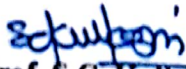
a) To discuss and approve paper setters' and examiners' panel for courses of F. Y. M. Tech. (E&Tc).

BoS discussed and approved Paper setters' and examiners' panels for courses of F. Y. M. Tech. (E&Tc)- *even Semester* for the academic year 2014-15 with certain modifications which are submitted to Assistant CoE (Pre-exam).

b) To discuss and approve the detailed syllabus of S. Y. M. Tech. - Electronics and Telecommunication (Digital System).

BoS discussed and approved the detailed syllabus of S. Y. M. Tech. - Electronics and Telecommunication (Digital System) with some modifications. Approved syllabus of S. Y. M. Tech. - Electronics and Telecommunication (Digital System) with suggested modifications is enclosed as **annexure - XIII**.

Meeting concluded with vote of thanks by Prof. S. C. Kulkarni


Prof. S.C. Kulkarni
(Member Secretary BoS E&TC)


Prof. S.O. Dahad
(Chairman BoS E&TC)

Copy To: 1. All members of BoS - E and T/C Engineering, GCoE, Jalgaon.
2. Hon. Principal and Chairman - Academic Council, GCoE, Jalgaon .
3. Hon. Chairman, Academic Programme Evaluation Committee, GCoE, Jalgaon